

**FIG. 1**

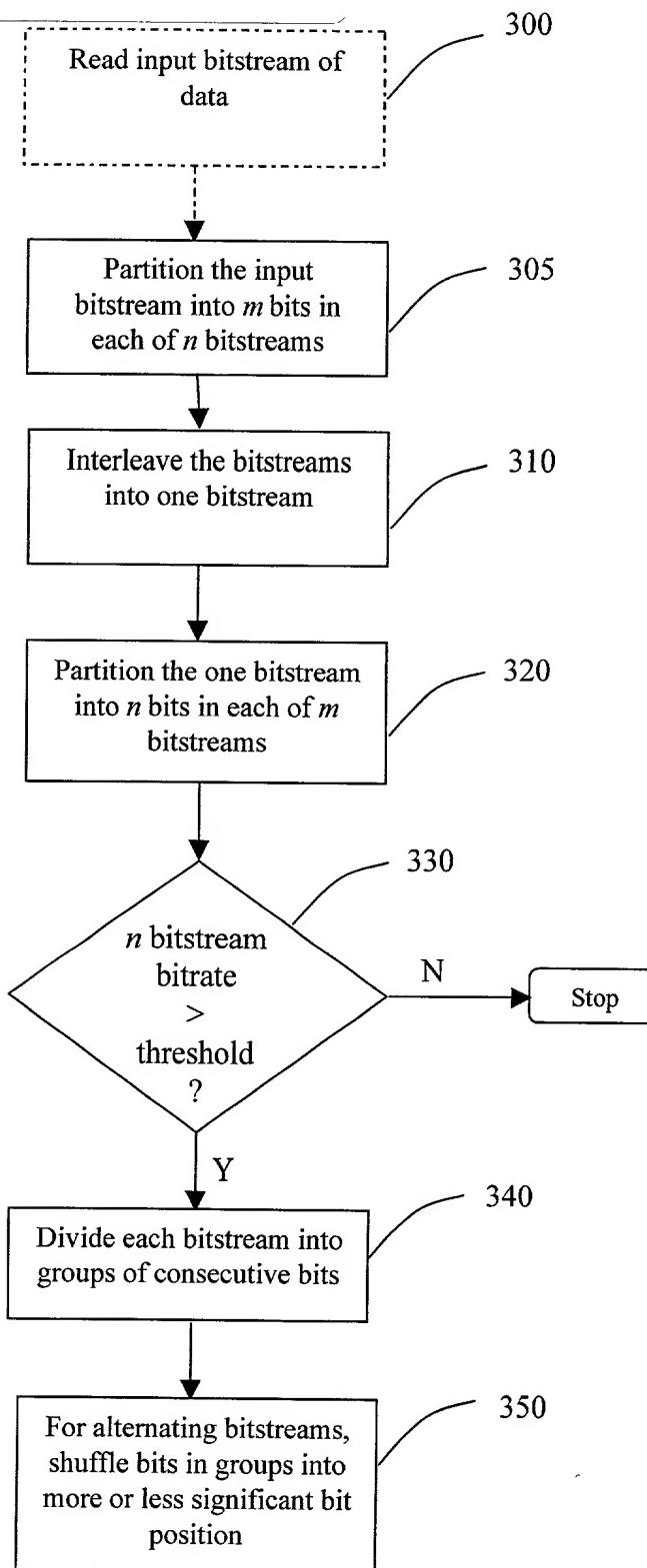
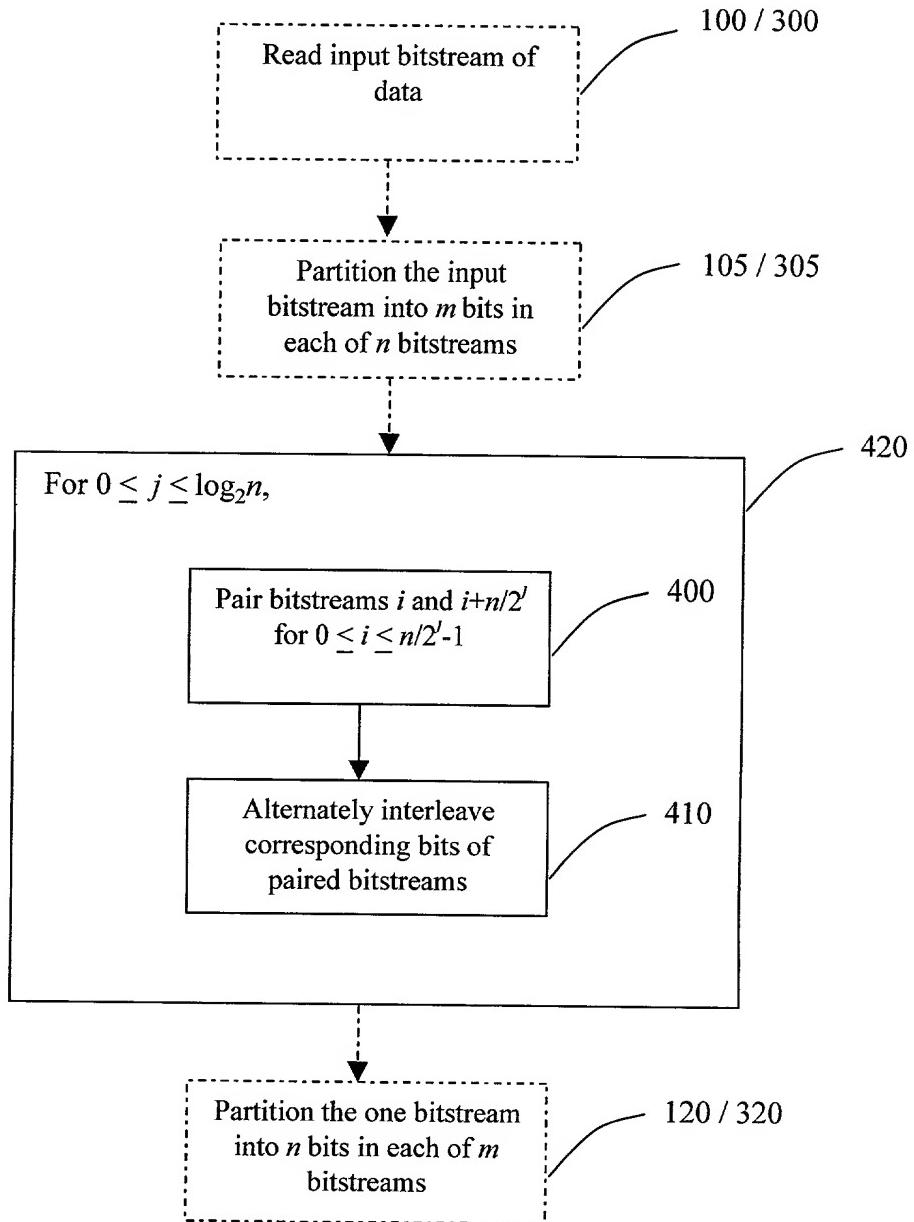


FIG. 2

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**FIG. 3**

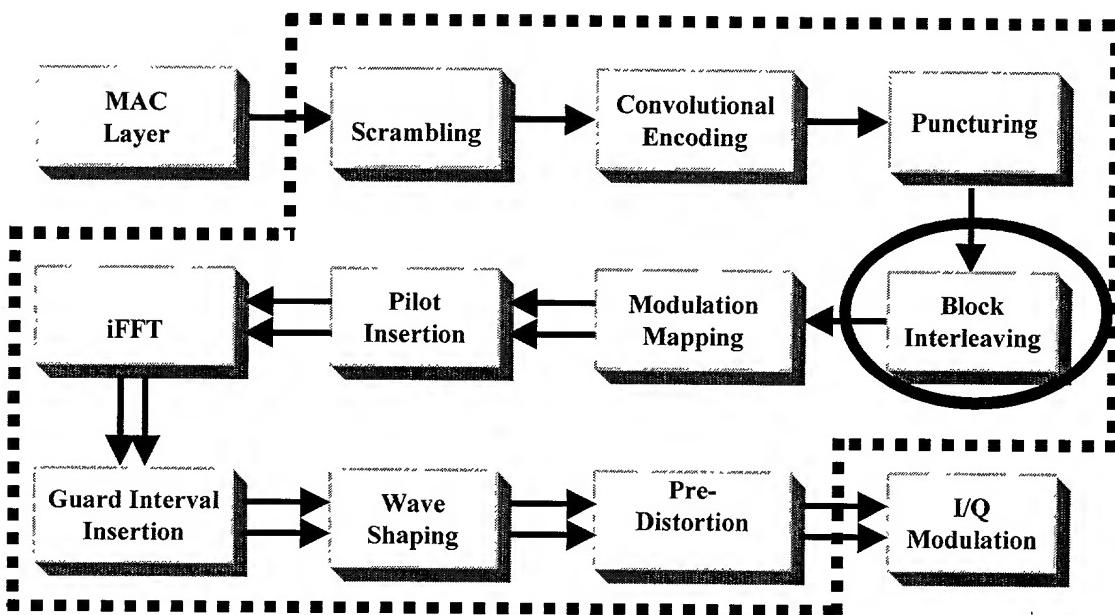


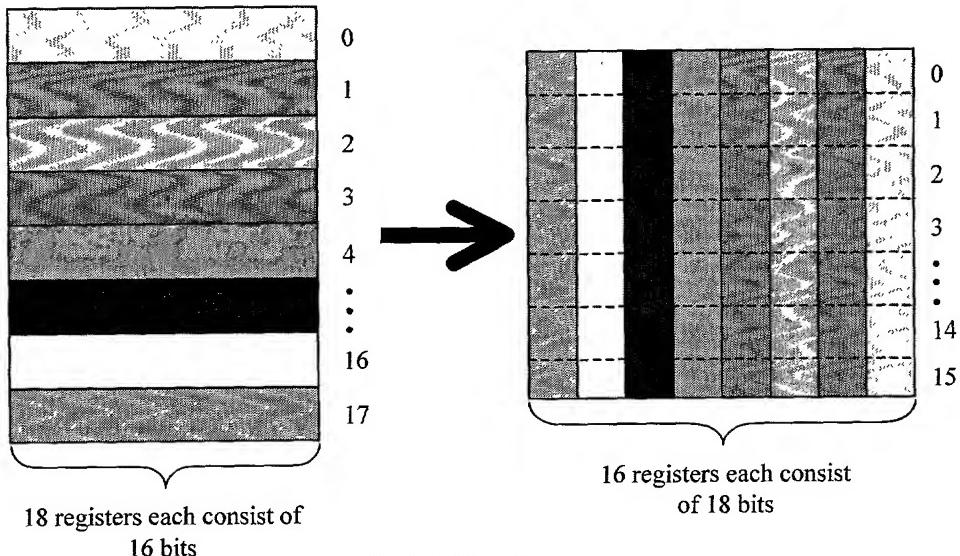
FIG. 4

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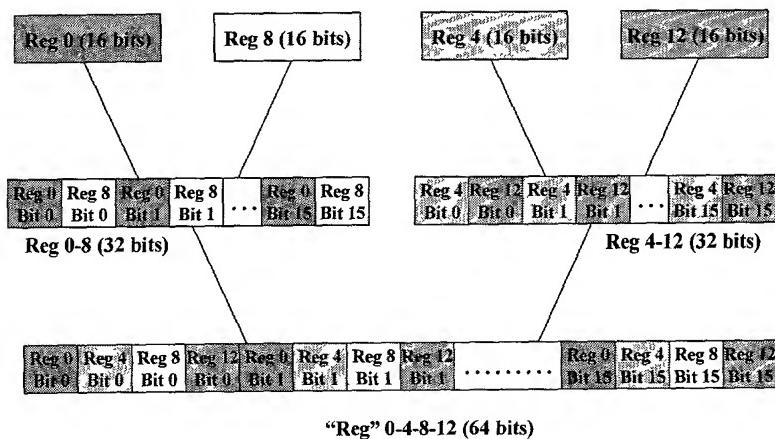
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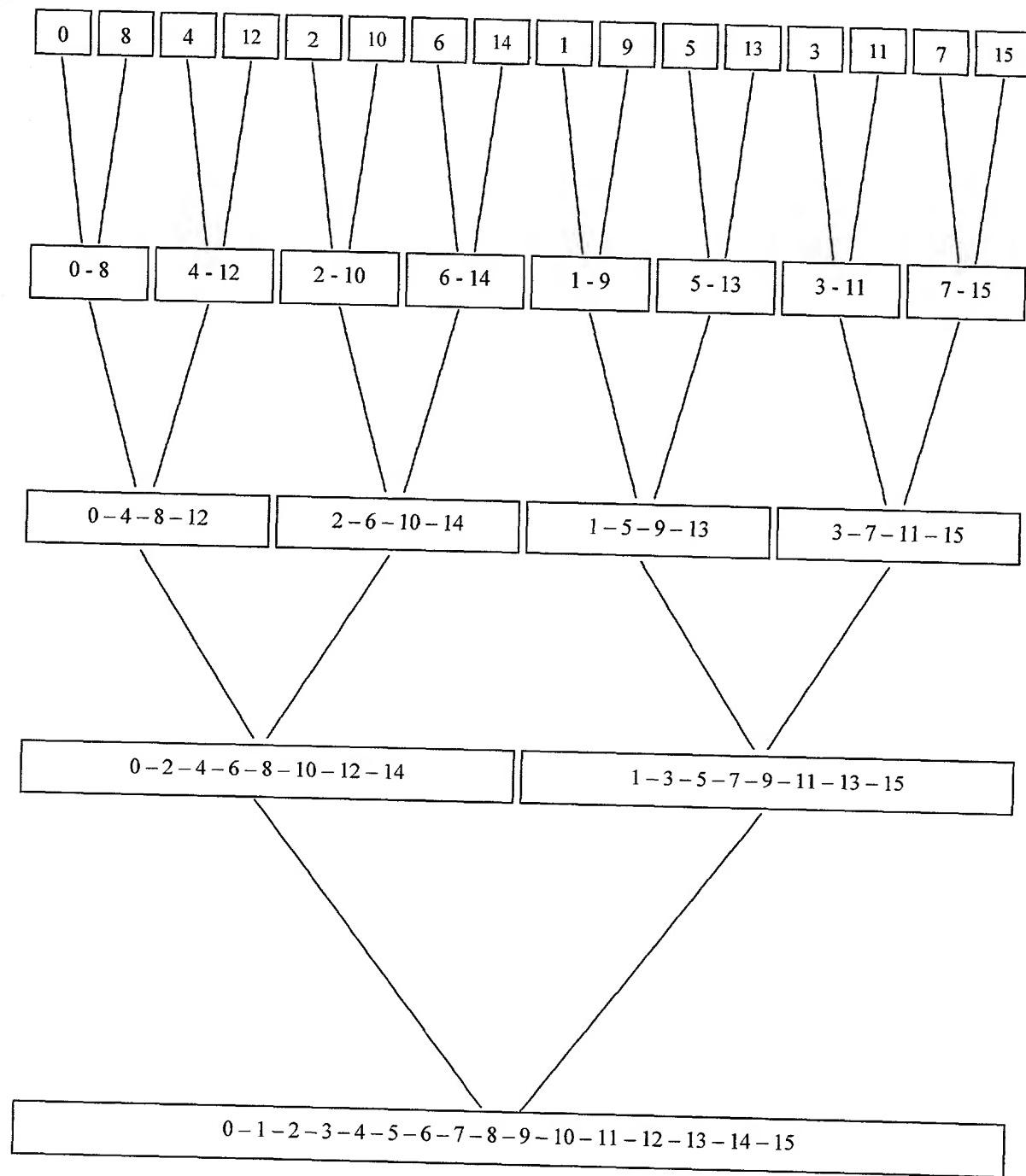
**FIG. 5**



Starting from Reg 0-8 then bit-interleaving with Reg 4-12 inserts bit 0 of Reg 4 between those of Reg 0 and 8, pushing bit 0 of Reg 8 to the right, and so on .... until the bits are ordered consecutively, from 0 to 15

**FIG. 7**

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**FIG. 6**

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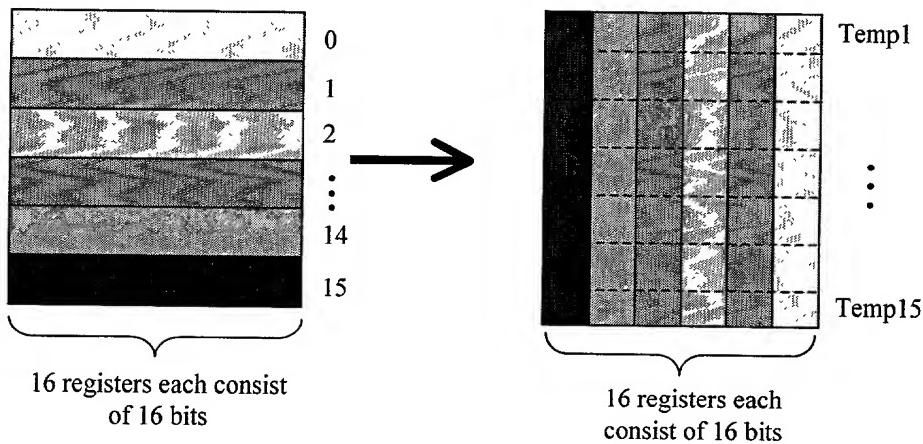


FIG. 8

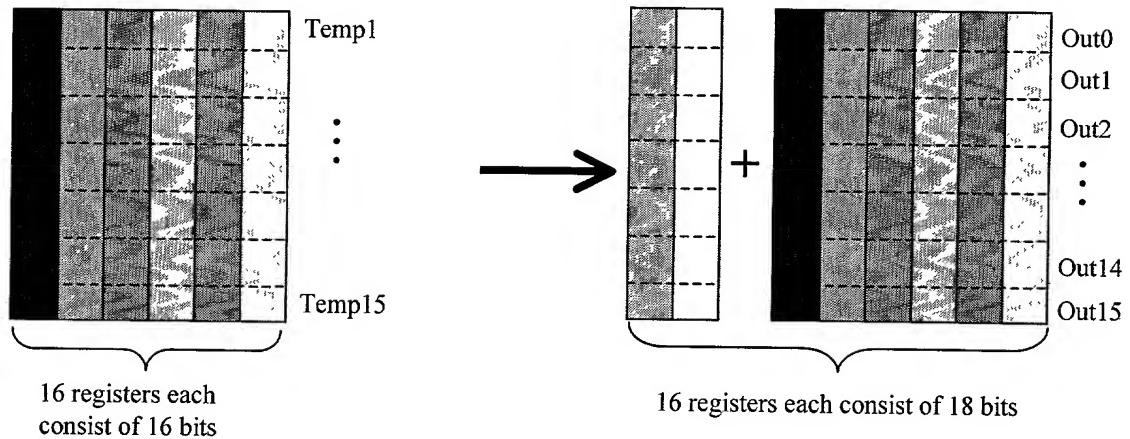
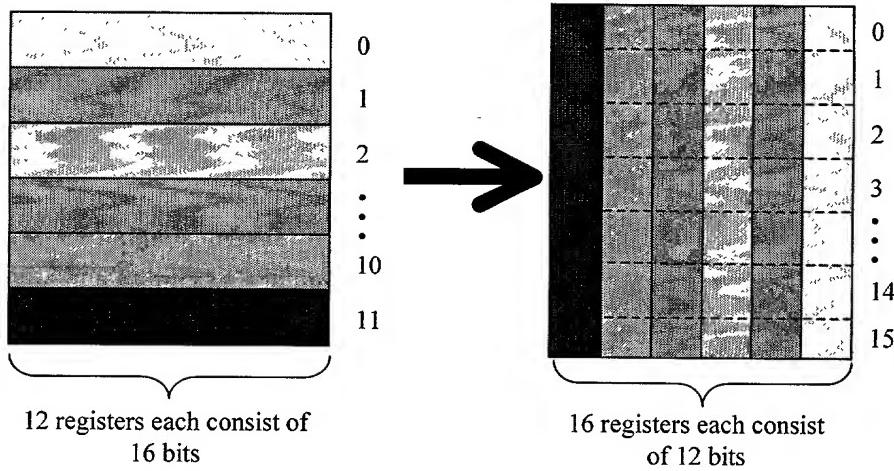
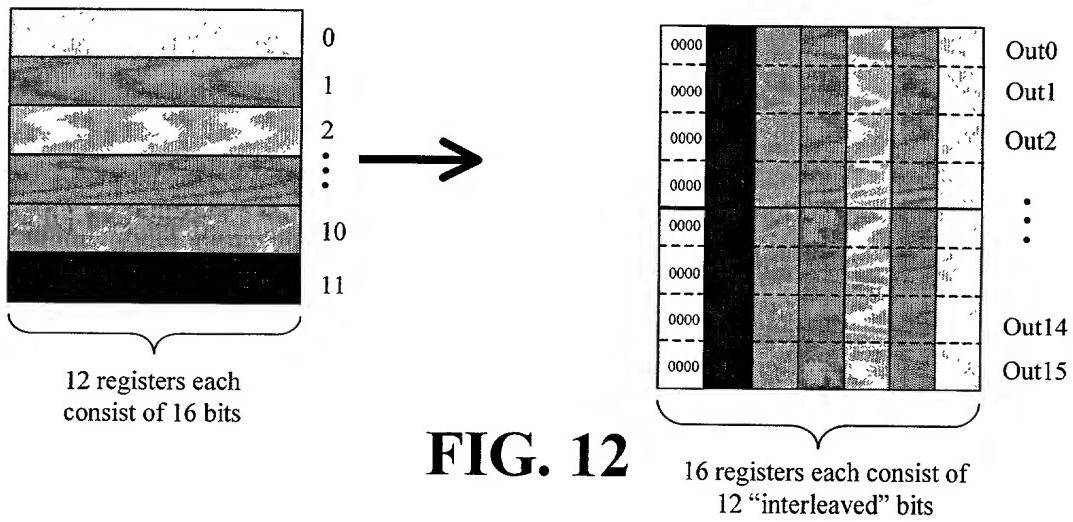


FIG. 9

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**FIG. 10**



**FIG. 12**

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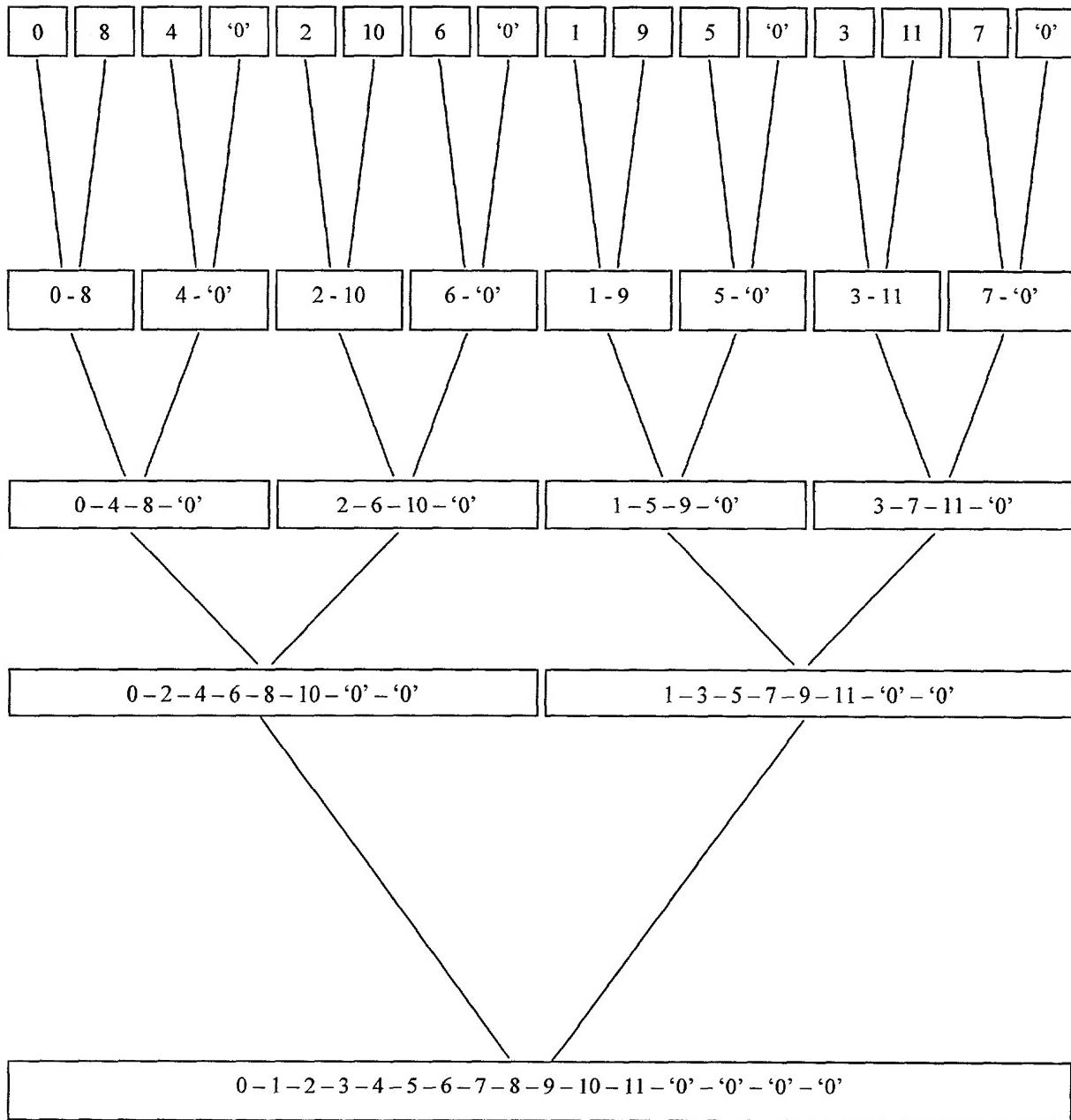
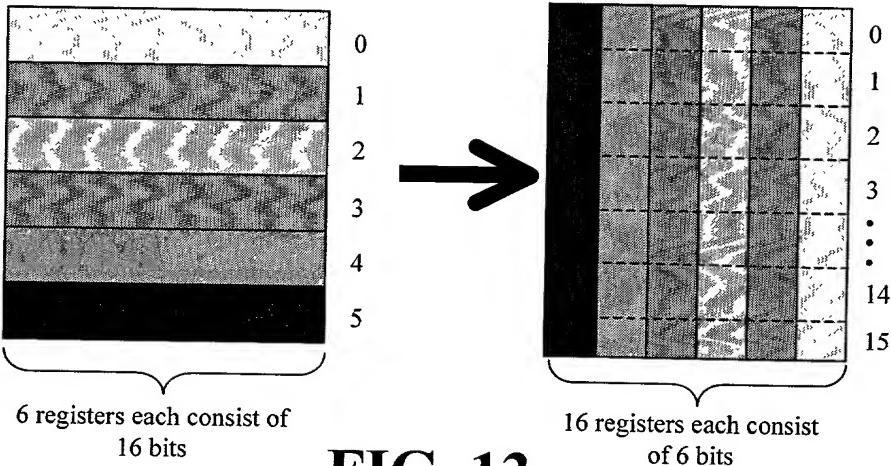
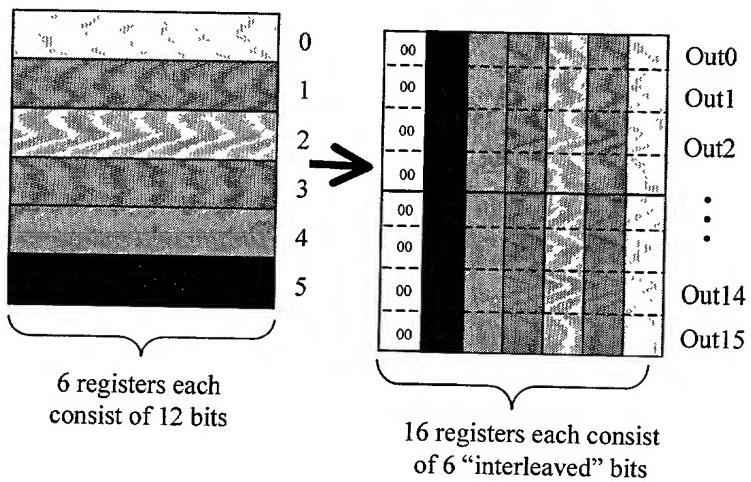


FIG. 11

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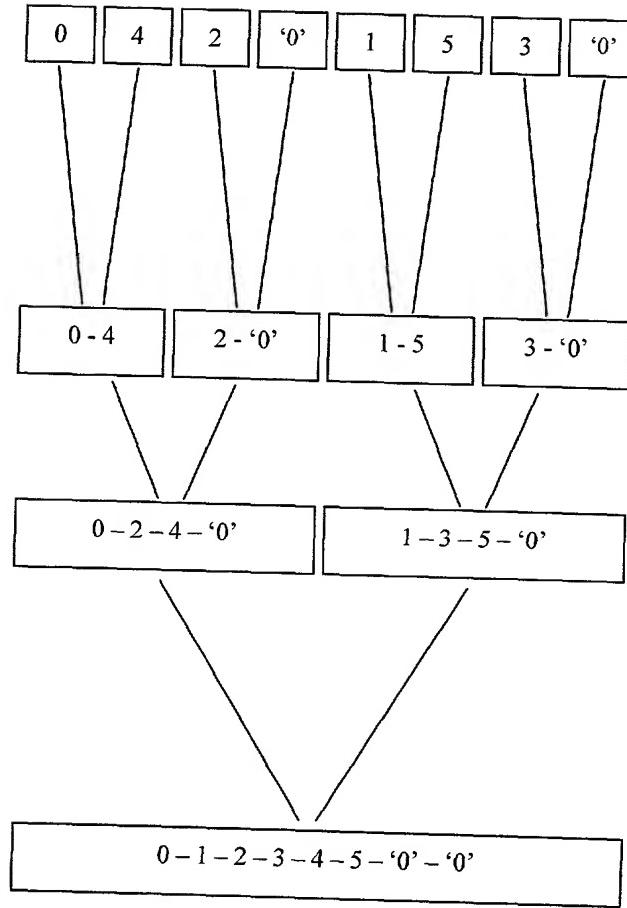
**FIG. 13**



**FIG. 15**

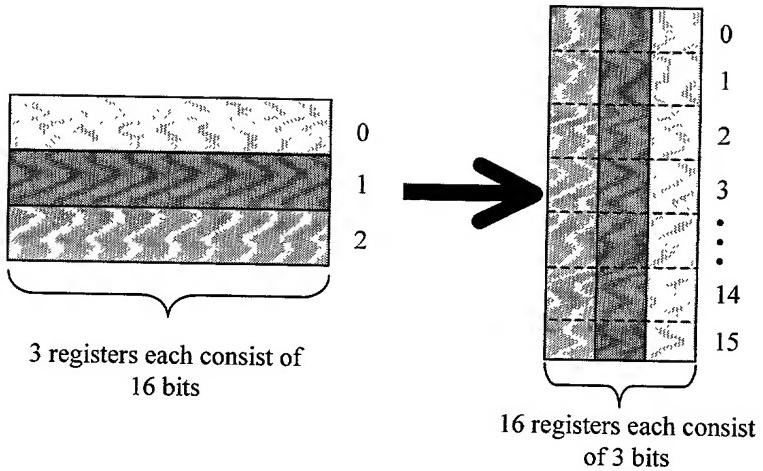
Title: BLOCK INTERLEAVER METHOD AND SYSTEM  
Inventor: Amit DAGAN  
Client/Matter: 2207/12613  
Serial No.: To be Assigned

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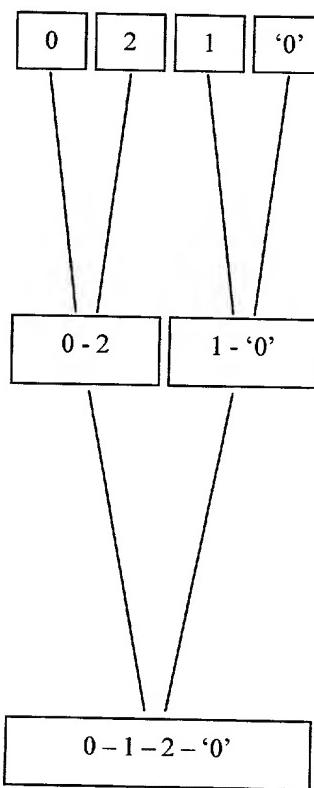


**FIG. 14**

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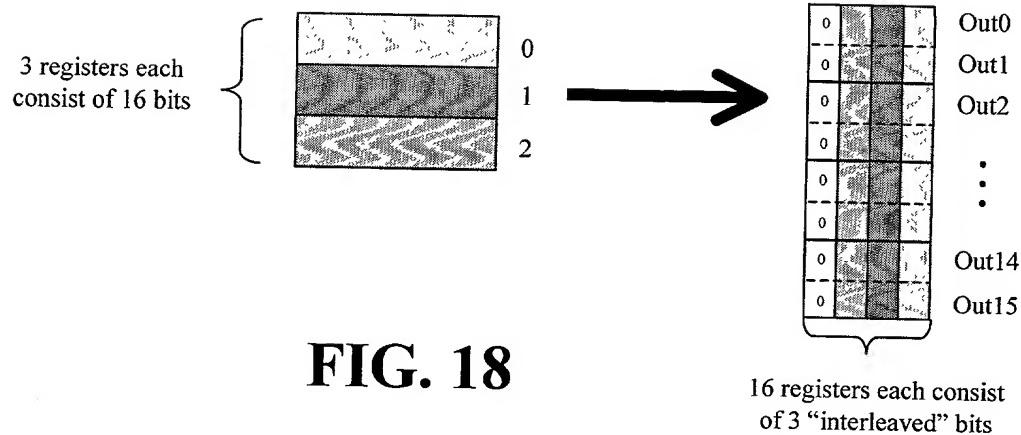


**FIG. 16**

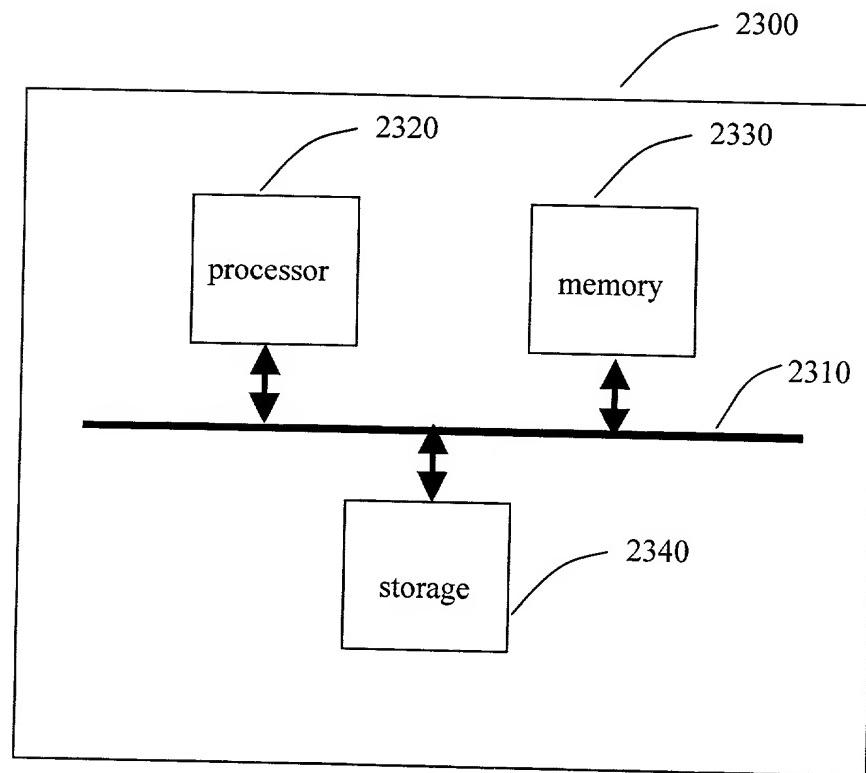


**FIG. 17**

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**FIG. 18**



**FIG. 19**